URN etd-0407109-183725-2130
Author Kun-Jou Ding
Author's Email Address No Public.
Statistics This thesis had been viewed 55 times. Download 0 times.
Department
Year 2006
Semester 1
Degree Master
Type of Document Master's Thesis
Language zh-TW.Big5 Chinese
Title Prototype Design of Test Pattern Generator for Testing TFT-LCD Panels
Date of Defense 0000-00-00
Page Count 66
Keyword
TFT-LCD

Abstract
With the development of Thin Film Transistor Liquid Crystal Display (TFT LCD),request of TFT-LCD Panel with higher advanced quality is correspondingly increase. The quality of TFT-LCD Panel will be commonly detected by using testing pattern which was produced by external Test Pattern Generator (TPG). Test Pattern Generator is currently on PC Base System. The advantage is that can provide programmable testing patterns, but price and volumeare relatively high.Therefore we propose a μc + FPGA (Field Programmable Logic Device) base system instead. This system can be modeled and parametric for various specification requirements.

In this paper, we inspect the various defects of TFT-LCD Panel such as DOT、Line Defect, Mura, etc, and design 6 basic samples of defects, Raster, Window, Colorbar, Checker, Grayscale and Character. We combine the 6 samples based on the algorithm to generate at least (C26x256x256x1) patterns. This system could be equivalent to a PC Base System to provide complicated patterns for overall defect inspections.
Advisory Committee Jing-Jou Tang - advisor

Files No Any Full Text File.
Date of Submission 2007-01-29